

Student Assistant (m/f/)*

Migration, Maintenance and Development of Netlist Reverse Engineering Software

Hardware netlist reverse engineering is the process of extracting the design information from an existing hardware circuit. This can be done by analyzing the physical structure of the circuit and mapping it back to its original design. The final step is the interpretation of the resulting netlist. For this purpose, our team has developed specific software. The main responsibilities of this working student position will include assisting with the migration of existing software, supporting the open-sourcing of the software, maintaining the software, and contributing to its further development.

Requirements

- Motivation to learn, or experience with:
 - Python
 - Hardware description languages (e.g. VHDL, Verilog)
 - Hardware Synthesis

Contact

We are constantly looking for new student team members that are excited about hardware security. Please send your application via e-mail with your CV, and most recent certificates and grades to the contact below. We are excited to meet you!

Johanna Baehr

Fraunhofer Institute for Applied and Integrated Security AISEC

Hardware Security

Lichtenbergstr. 11, 85748 Garching near Munich

Mail: johanna.baehr@aisec.fraunhofer.de

Phone: +49 89 322 9986-1006

Publication Date: 06.06.2024